

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A signal bounce inhibiting device for preventing ~~from~~ power/ground bounce resulting from signal transmission from a first chip to a second chip, comprising:

an electric level toggling circuit receiving a first internal signal ~~of a~~ from said first chip, and toggling said first internal signal with a first electric level into a first output signal with a second electric level in response to a first toggling control signal ~~inputted therein~~; and

an electric level recovering circuit receiving said first output signal with said second electric level, and recovering said first output signal with said second electric level into said first internal signal with said first electric level ~~required by a second chip~~ in response to a first recovering control signal ~~inputted therein~~, and providing said first internal signal with said first electric level for said second chip.

2. (Original) The signal bounce inhibiting device according to claim 1 wherein said electric level toggling circuit is disposed in an output stage of said first chip and said electric level recovering circuit is disposed in an input stage of said second chip.

3. (Currently Amended) The signal bounce inhibiting device according to claim 1 wherein said electric level toggling circuit further receives a second internal signal from said first chip, which and remains ~~said second internal signal~~ unchanged as ~~a second output signal~~ in response to a second toggling control signal ~~inputted therein~~ and is outputted as a second output signal identical to said second internal signal, and said electric level recovering circuit ~~remains~~ receives said second output signal from said electric level toggling circuit, which remains identical to said second internal signal and is provided for said second chip unchanged in

~~response to a second recovering control signal inputted therein so as to provide said second internal signal for said second chip.~~

4. (Currently Amended) The signal bounce inhibiting device according to claim 3 wherein said electric level toggling circuit includes a ~~first count~~ plurality of electric level toggling units for toggling or untoggling a plurality of a second count of said first internal signals in response to ~~said second count of first~~ respective toggling control signals, ~~and remaining a third count of said second internal signals into said second output signals in response to said third count of second toggling control signals.~~

5. (Currently Amended) The signal bounce inhibiting device according to claim 4 wherein said electric level recovering circuit includes ~~said first count~~ a plurality of electric level recovering units for recovering said plurality of internal signals by changing said toggled internal signals and holding said untoggled internal signals ~~said second count of said first output signals into said first internal signals in response to said second count of said first recovering control signals, and remaining said third count of said second output signals into said second internal signals in response to said third count of said second~~ respective recovering control signals.

6. (Currently Amended) The signal bounce inhibiting device according to claim 3 wherein said electric level toggling circuit includes ~~a plurality of electric level toggling units, each of which includes:~~

a register for ~~inputted therein~~ receiving and storing a certain toggling control signal; and
an XOR gate for receiving said certain toggling control signal and a certain internal signal to perform a first XOR operation, thereby resulting in a toggled or an untoggled output signal ~~toggling said certain internal signal or remaining said certain internal signal unchanged so as to obtain a certain output signal.~~

7. (Currently Amended) The signal bounce inhibiting device according to claim 6 wherein said electric level recovering circuit includes ~~a plurality of electric level recovering units, each of which includes:~~

a register for ~~inputted therein~~ receiving and storing a certain recovering control signal corresponding to said certain toggling control signal; and

an XOR gate for receiving said certain recovering control signal and said ~~certain toggled or untoggled~~ output signal to perform a second XOR operation, thereby ~~toggling or remaining~~ said certain output signal so as to recover said certain recovering said toggled or untoggled output signal into said certain internal signal.

8. (Currently Amended) The signal bounce inhibiting device according to claim 7 wherein said first ~~and said second~~ toggling control signals are signal is identical to said first ~~and said second~~ recovering control signals signal, and said second toggling control signal is identical to said second recovering control signal.

9-11. (Cancelled)

12. (Currently Amended) A signal bounce inhibiting device embedded in ~~an~~ a first integrated chip for preventing signals to be transmitted to a second integrated chip from power/ground bounce, comprising:

an electric level toggling circuit toggling a first internal signal with a first electric level into a first output signal with a second electric level in response to a first toggling control signal and ~~remaining~~ outputting a second internal signal with an electric level remaining unchanged as a second output signal in response to a second toggling control signal; and

a storing device for storing said first toggling control signal and said second toggling control signal,

wherein said first output signal with said second electric level is subsequently recovered to said first internal signal with said first electric level in said second integrated chip according to said first toggling control signal.

13. (Currently Amended) The signal bounce inhibiting device according to claim 12 wherein said electric level toggling circuit includes a ~~first-count~~ plurality of electric level toggling units for receiving a plurality of internal signals and toggling some of said internal signals while untoggling the other internal signals in response to respective toggling control signals simultaneously received ~~toggling a second count of said first internal signals into said first output signals in response to said second count of first toggling control signals, and remaining a third count of said second internal signals into said second output signals in response to said third count of second toggling control signals.~~

14. (Currently Amended) The signal bounce inhibiting device according to claim 12 wherein said electric level toggling circuit includes a plurality of electric level toggling units, each of which includes an XOR gate performing a first XOR operation in response to ~~for receiving~~ said first toggling control signal and said first internal signal ~~to perform a first XOR operation~~, thereby toggling said first internal signal, or performing a second XOR operation in response to ~~for receiving~~ said second toggling control signal and said second internal signal ~~to perform a second XOR operation~~, thereby ~~remaining~~ untoggling said second internal signal unchanged.

15. (Currently Amended) The signal bounce inhibiting device according to claim 12 wherein said storing device includes a plurality of ~~storing units, each of which includes a register for inputting thereto and storing~~ registers, each of which stores therein said first toggling control signal or said second toggling control signal.

16-20. (Cancelled)

21. (New) A method for preventing from power/ground bounce resulting from signal transmission from a first chip to a second chip, comprising steps of:

asserting a plurality of toggling control signals;

toggling some of a plurality of internal signals and untoggling some of said plurality of internal signals in response to said toggling control signals;

asserting a plurality of recovering control signals correlating to said plurality of toggling control signals; and

recovering said plurality of internal signals by changing said toggled internal signals and holding said untoggled internal signals in response to said recovering control signals.

22. (New) The method according to claim 21 wherein said internal signals are toggled in response to first toggling control signals with a first level and recovered in response to first recovering control signals with said first level, and said internal signals are untoggled in response to second toggling control signals with a second level, and remains unchanged in response to second recovering control signals with said second level.